

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 6,993,504 B1

APPLICATION NO.: 09/651,301

ISSUE DATE : January 31, 2006

INVENTOR(S) : Richard W. Friesen, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 7, line 29: please delete "\$29.50" and insert --slightly less than \$27.35--.

Column 8, line 14: please delete "300(91)" and insert --300(1)--.

Column 8, lines 18-29: please delete the entire phrase "Thus by viewing all outstanding offer icons 304 and bid icons 300 as they are made on an item, the trader can anticipate the market and quickly adjust his or her trading plans to take advantage of the information. In contrast, in conventional systems, the trader only knows the last highest bid and the last highest offer. In the example of FIG. 3a, the trader would only know the existence of bid 300(9) and offer 304(1). Only the market maker would know the existence of the other bids and offers. Individual traders would therefore be unaware of trends in bidding, and experience greater difficulty in anticipating the market."

Column 8, line 55: please delete "is chosen" and insert --has chosen--.

Column 11, line 20: please delete "412" and insert --417--.

Column 11, line 23: please delete "412" and insert --417--.

Claim 26, col. 26, line 18: please insert --with-- between "associated" and "other."

Claim 36, col. 26, line 58: please delete "with eth current" and insert --with the current--.

Claim 49, col. 27, line 36: please delete "lien" and insert --line--.

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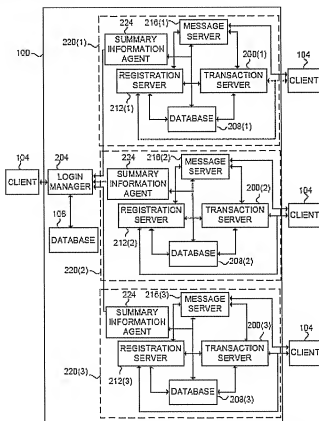


FIG. 2

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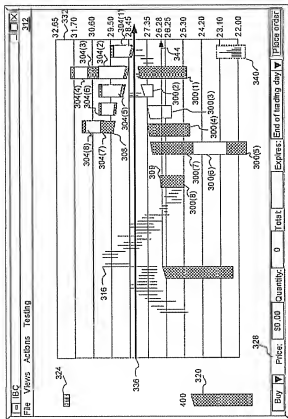


FIG. 3A

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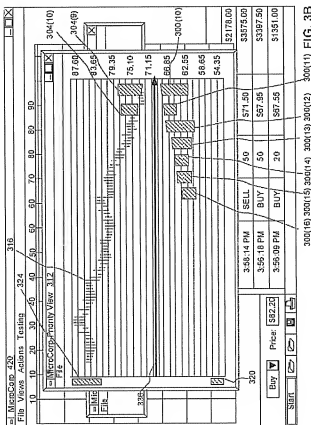
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It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

BUY

Price:

Quantity:

Value: \$1436.00

Expires:

FIG. 3D

SELL

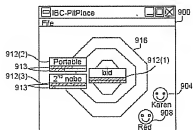
Price:

Quantity:

Value: \$2505.00

Expires:

FIG. 3E



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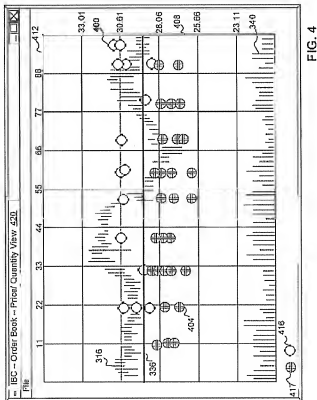


FIG. 4

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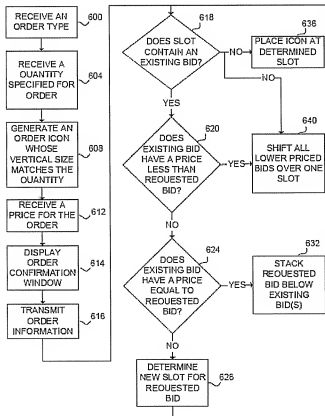


FIG. 6

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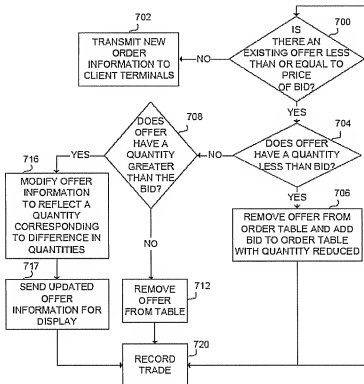


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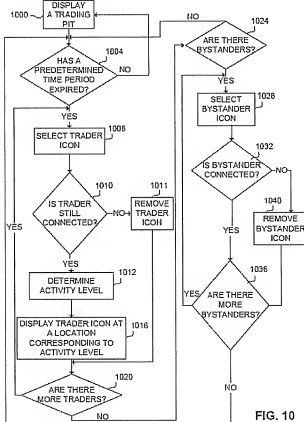


FIG. 10

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FIG. 12

FIG. 12 is a complex schematic diagram showing a hierarchical structure of components. It includes a top-level block 'Patent Pending' with sub-blocks 'PWA1', 'PWA2', 'PWA3', 'PWA4', 'PWA5', 'PWA6', 'PWA7', 'PWA8', 'PWA9', 'PWA10', 'PWA11', 'PWA12', 'PWA13', 'PWA14', 'PWA15', 'PWA16', 'PWA17', 'PWA18', 'PWA19', 'PWA20'. Below these are various other blocks like 'PWA1A', 'PWA1B', 'PWA1C', 'PWA1D', 'PWA1E', 'PWA1F', 'PWA1G', 'PWA1H', 'PWA1I', 'PWA1J', 'PWA1K', 'PWA1L', 'PWA1M', 'PWA1N', 'PWA1O', 'PWA1P', 'PWA1Q', 'PWA1R', 'PWA1S', 'PWA1T', 'PWA1U', 'PWA1V', 'PWA1W', 'PWA1X', 'PWA1Y', 'PWA1Z'. The diagram is labeled with various numbers and letters, including '1208', '1209', '1210', '1211', '1212', '1213', '1214', '1215', '1216', '1217', '1218', '1219', '1220', '1221', '1222', '1223', '1224', '1225', '1226', '1227', '1228', '1229', '1230', '1231', '1232', '1233', '1234', '1235', '1236', '1237', '1238', '1239', '1240', '1241', '1242', '1243', '1244', '1245', '1246', '1247', '1248', '1249', '1250'. The diagram is also labeled with 'FIG. 12' and 'FIG. 12'.

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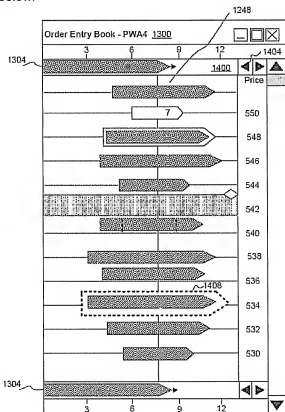


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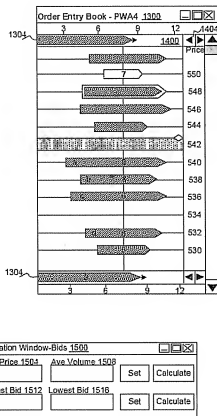


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